

Synchronization

- ordering of events across potentially concurrently executing code

a = 0
a = 1
b = a

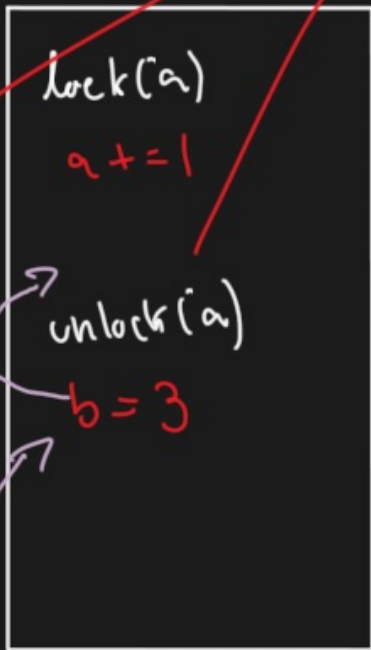
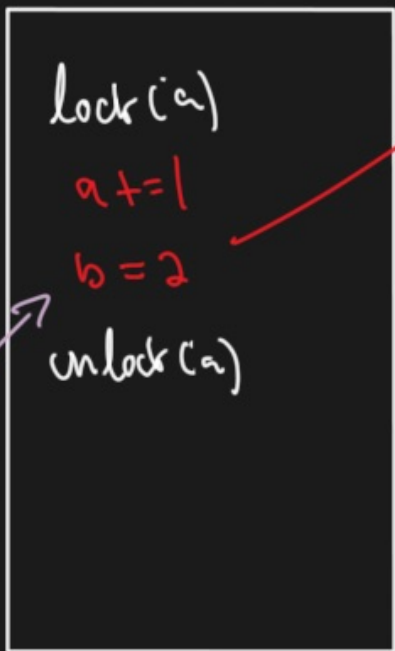
P₁

a = 2
a = 3
b = a

P₂

Locks

- lock(*) - unlock(*)

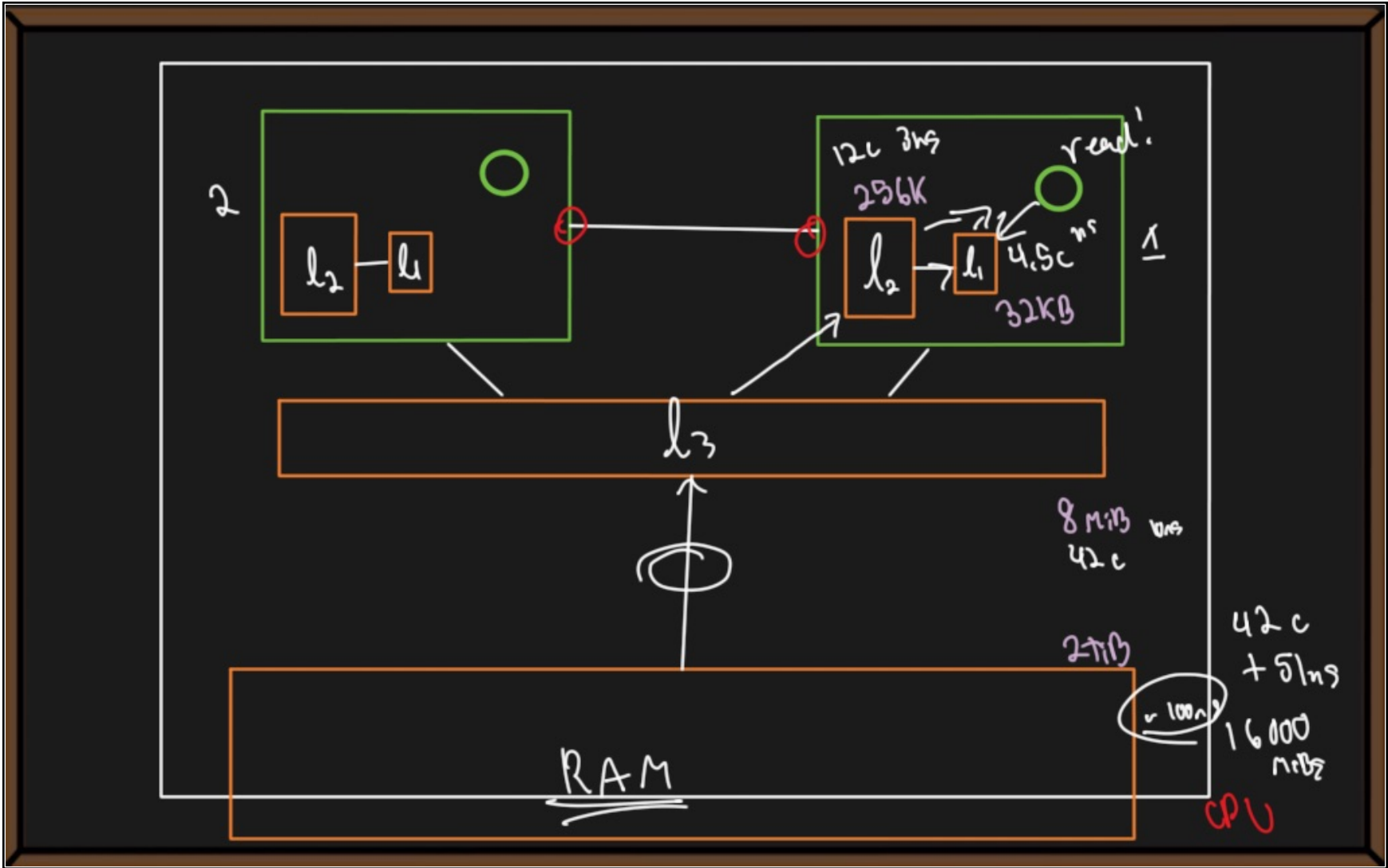


critical
section
lock
cmp -
xchg

```
struct Lock {
    bool locked;
};
```

```
fn lock(self &*) {
    while self.locked {
        // wait
    }
    self.locked = true;
}

fn unlock(self &*) {
    self.locked = false;
}
```



MOESI

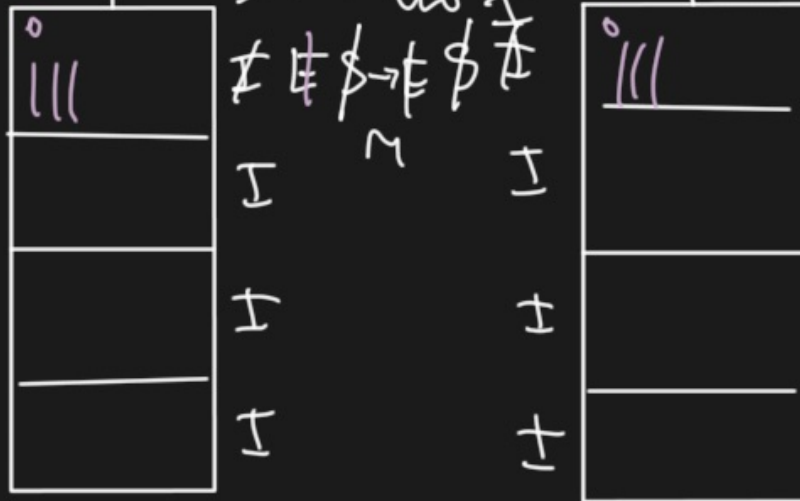
MESI

I = invalid
 S = shared
 = read only
 E = exclusive
 = r/w
 M = modified
 = r/w



coherence (cache)

P₁ reads 0x10 (C10)
 P₁ write to 0x04
 P₂ reads 0x10 (C10)



P₁ cache

P₂ cache

Weak
 Consistency

64b

TSO

ⓐ A → B → C

work
throughput

